

**Fig. 1**

## Conditioning Accumulator Inputs

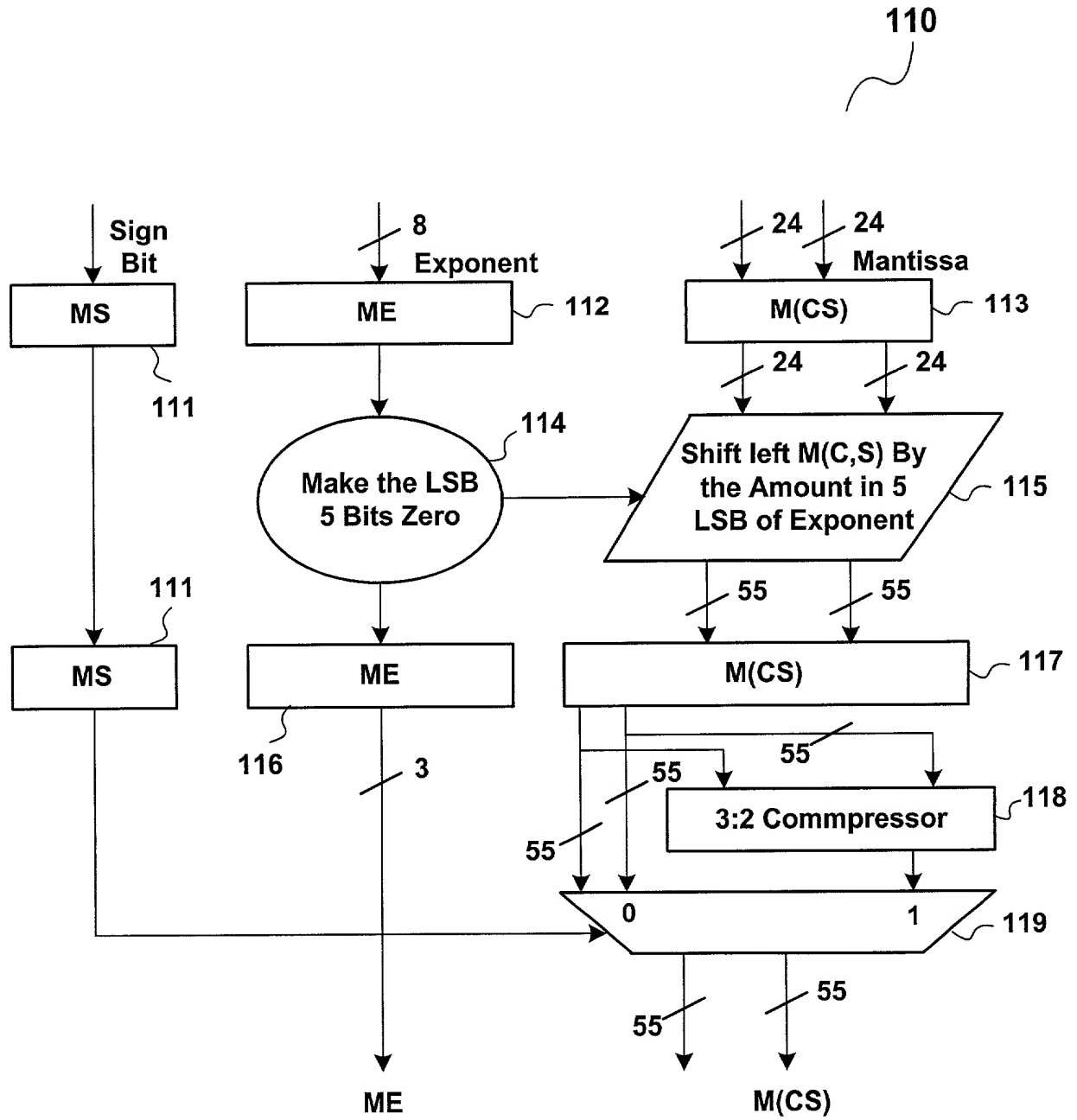
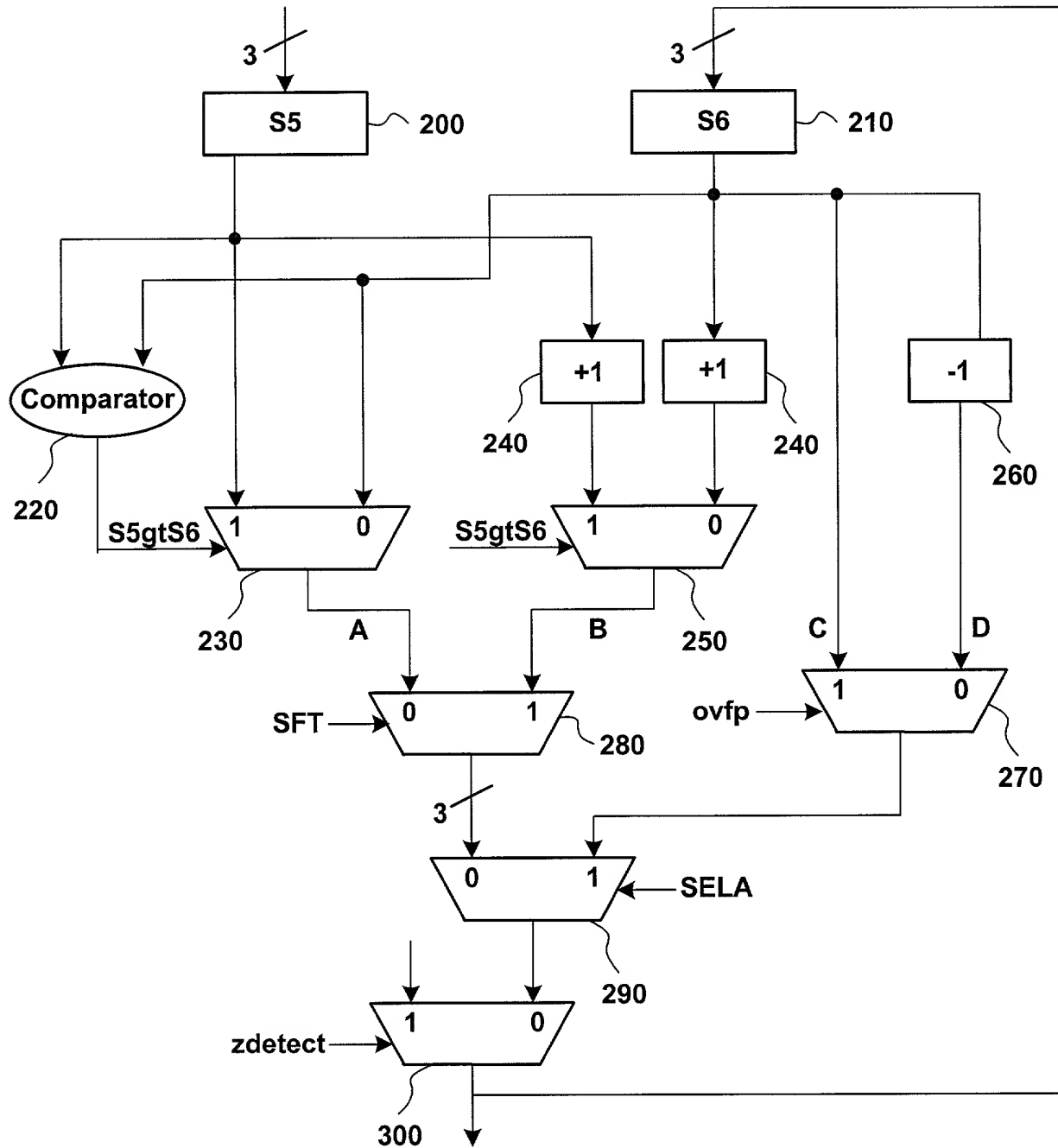


Fig. 2

**Exponent Loop****Fig. 3**

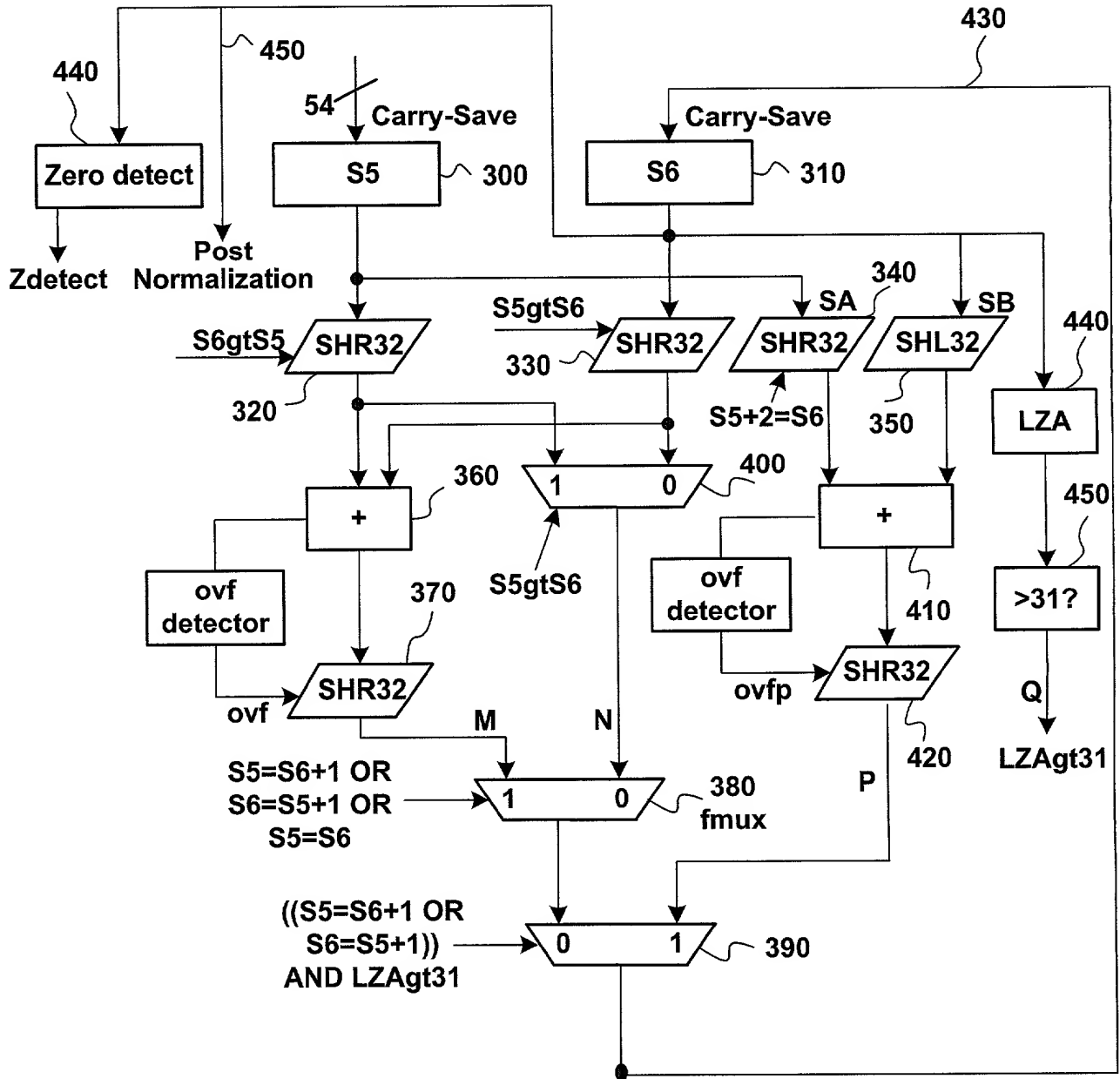


Fig. 4

## Postnorm

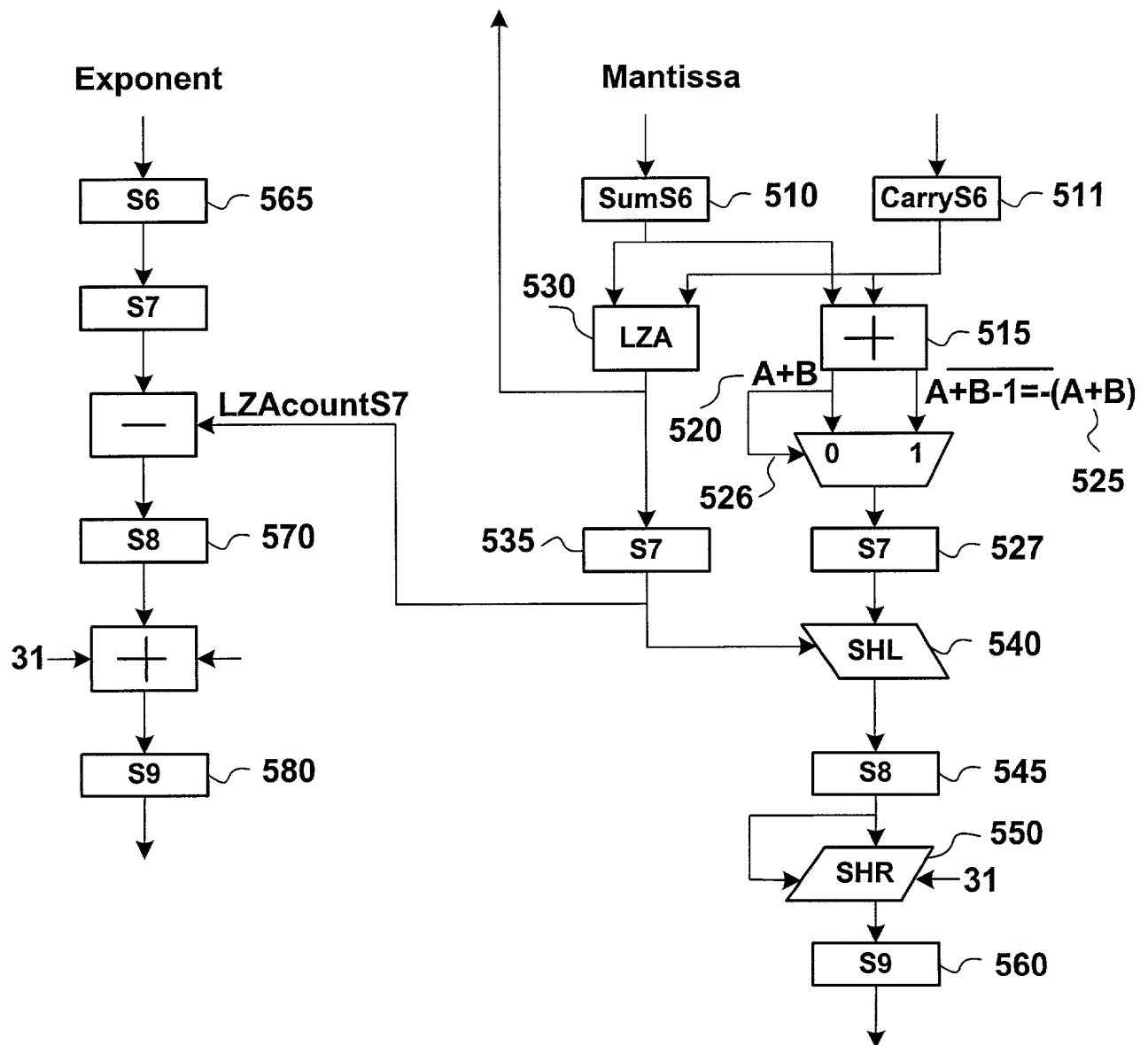


Fig. 5